ISSUE CLASSIFICATION	
Class	Subclass
257	782

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED 6/4 Q.A. AG	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/768911		~~257~~ 438	782	~~2811~~ 2827	ZARNEKE

APPLICANTS

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James Bales
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TITLE

Integrated circuit package [illegible] reverse engineering and method for fabricating the same using vias without metal terminations

PTO-2040
12/99

ISSUING CLASSIFICATION

DRAWINGS			TOTAL CLAIMS	INDEPENDENT CLAIMS
Sheets Drwg.	Figs. Drwg.	Print Fig.	21	1
1	4	9		

☐ The term of this patent subsequent to _____ (date) has been disclaimed.	David A. Zarneke (Assistant Examiner) 12/9/02 (Date)	NOTICE OF ALLOWANCE MAILED 12/18/02
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	ALBERT W. PALADINI PRIMARY EXAMINER Albert W. Paladini (Primary Examiner) 12-13-02 (Date)	ISSUE FEE: Amount Due $1,580.00; Date Paid
☐ The terminal ___ months of this patent have been disclaimed.	J. McMillan (Legal Instruments Examiner) 12/16/02 (Date)	ISSUE BATCH NUMBER

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DRAWINGS IN FILE

(FACE)